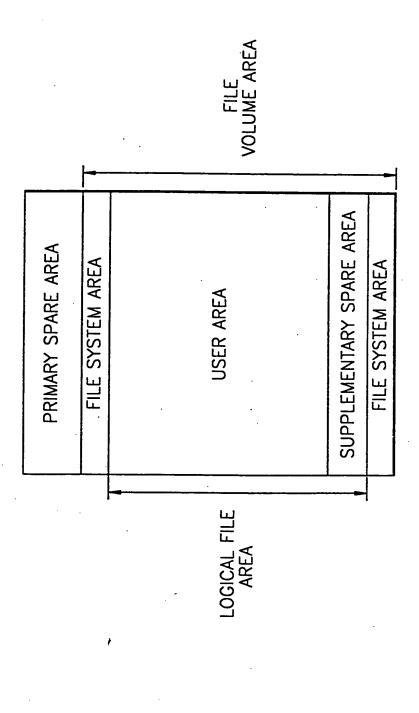
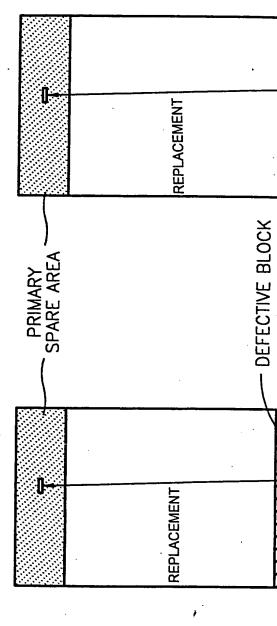
FIG. 1 (PRIOR ART)

		b23
22	Group22 Group	roup23 Group

FIG. 2





AREA WHICH IS NOT ALLOCATED AS SUPPLEMENTARY SPARE AREA BUT HAS BEEN USED AS USER DATA AREA

SUPPLEMENTARY SPARE AREA

FIG. 4A

32K BUFFER MEMORY MODEL

Recommended spare area	곳	2.8	2.7	2.5	2.4	2.3	2.1	2.0	1.8	1.7	1.5	1.4	1.3	1.	1.0	0.8
	+48B	4096	3888	3680	3472	3264	3056	2848	2640	2432	2224	2016	1808	1600	1392	1184
	듯	2.8	2.7	2.5	2.4	2.2	2.1	1.9	1.7	1.6	1.4	1.3	1.1	1.0	0.8	0.7
	+32BLK	4096	3872	3648	3424	3200	2976	2752	2528	2304	2080	1856	1632	1408	1184	096
Total no.	oerects	4348	4604	4860	5116	5372	5628	5884	6140	6396	6652	8069	7164	7420	7676	7932
pare)	%	2.7	2.5	2.4	2.2	2.1	1.9	1.8	1.6	1.5	1.3	1.1	1.0	0.8	0.7	0.5
Min. Required Spare area(32K)	MB	127.8	120.5	113.1	105.8	98.5	91.1	83.8	76.4	69.1	61.8	54.4	47.1	39.7	32.4	25.1
	Blocks	3901	3677	3453	3229	3005	2781	2557	2333	2109	1885	1661	1437	1213	989	765
Min. Alloc.		320	352	384	416	448	480	512	544	576	608	640	672	704	736	768
Max. no. of SDL Entries		3837	3581	3325	3069	2813	2557	2301	2045	1789	1533	1277	1021	765	509	253
mber of Fatries		- 511	- 1023	- 1535	- 2047	- 2559	- 3071	- 3583	- 4095	- 4607	– 5119	- 5631	- 6143	- 6655	- 7167	- 7679
Number of PDI Entries		- 0	512 -	1024 -	1536 -	2048 -	2560 -	3072 -	3584 -	4096 -	4608 -	5120	5632 -	6144 -	6656 -	7168 –

FIG. 4B

64K BUFFER MEMORY MODEL

				,	_		,			_						
Recommended spare area	뜻	2 8	2.9	2.9		3.0	3.0	3.0	3.1	.3	3.1	3.2	3.2	3.2	3.3	3.3
	+48BLK	4096	3888	3680	3472	3264	3056	2848	2640		2224	2016	1808	1600	1392	1184
	۲	2.8	i	2.9	2.9	2.9	2.9	3.0	3.0	1	3.0	3.1	3.1	3.1	3.1	3.1
Recor	+32BLK	4096	4128	4160	4192	4224	4256	4288	4320	1	1	4416	4448	4480	4512	4544
Total no.	gerects	4348	4860	5372	5884	6396	8069	7420	7932	8444	8956	9468	9980	10492	11004	11516
pare)	%	2.7	2.7	2.7	2.8	2.8	2.8	2.8	2.9	2.9	2.9	2.9	2.9	3.0	3.0	3.0
Min. Required Spare area(64K)	MB	127.8	128.9	129.9	131.0	132.0	133.1	134.1	135.2	136.2	137.3	138.3	139.4	140.4	141.5	4349 142.5
Min. Re are	Blocks	3901	3933	3965	3997	4029	4061	4093	4125	4157	4189	4221	4253	4285	4317	4349
Min. Alloc.		320	352	384	416	448	480	512	544	576	608	640	672	704	736	768
Max. no. of SDL Entries		3837	3837	3837	3837	3837	3837	3837	3837	3837	3837	3837	3837	3837	3837	3837
Number of PDL Entries		0 - 511	512 - 1023	1024 - 1535	1536 - 2047	2048 - 2559	2560 - 3071	3072 - 3583	3584 - 4095	4096 - 4607	4608 - 5119	5120 - 5631	5632 - 6143	6144 - 6655	6656 - 7167	7168 – 7679

FIG. 5A

RESERVED BITS		FIG. 5D		RESERVED BITS	
PRIMARY SPARE AREA REMAINDER INFORMATION	2 bits		,	SUPPLEMENTARY SPARE AREA REMAINDER INFORMATION	77,7 0

FIG. 6

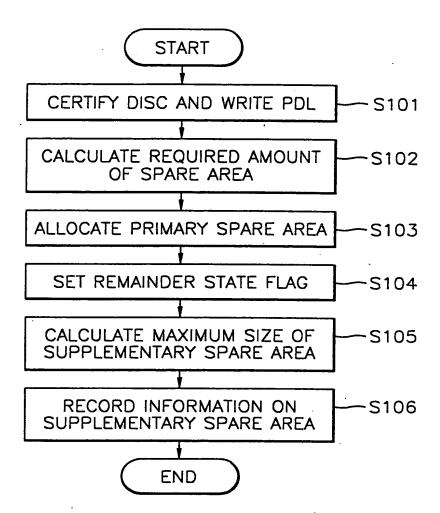


FIG. 7

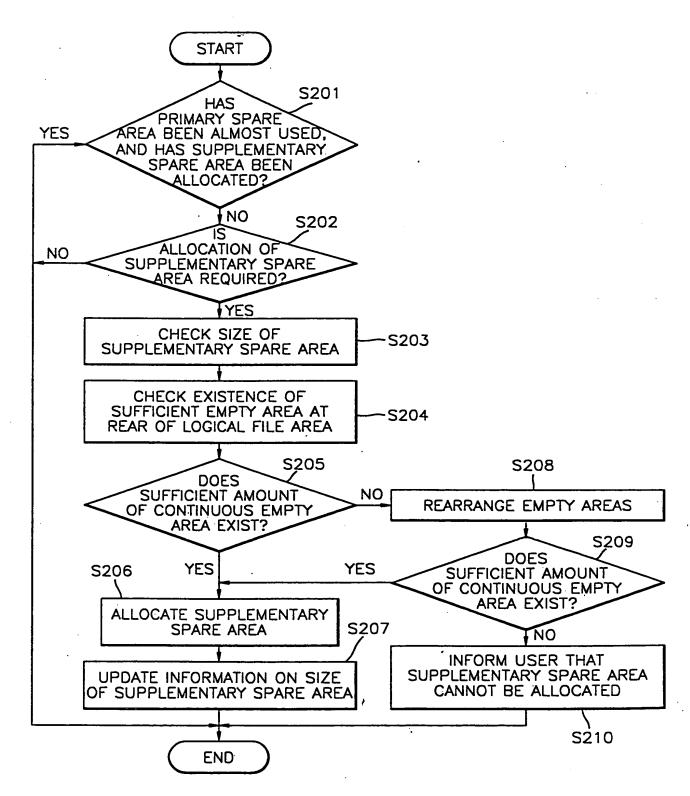


FIG. 8

